

## Microelectronics Presentation Days 2010

schedule		1st Day (March 30th, 2010)		
start time	end time	Company	Activity	Presenter
10:00	10:10	ESA	Welcome	Agustin Fernandez-Leon
10:10	10:30	Atmel (F)	ATC18RHA Multi-Project Wafer	Dominique de Saint Roman
10:30	10:50	ESA	Overview of ESA activities on FPGA technology	David Merodio Codinachs
10:50	11:10	CNES/Atmel (F)	FPGA 280K, 450K, modules	Bernard Bancelin
11:10	11:30		coffee break	
11:30	12:00	IMEC (B)	DARE180nm maintenance & DARE90nm development	Geert Thys
12:00	12:20	TAS-ETCA (B) / IMEC(B) / Aeroflex Gaisler (S)	DARE (Design Against Radiation Effects) ESCC Evaluation, LEON3-DARE	Pierre Somerlinck
12:20	12:40	CMOSIS (B)	SUN-Sensor-on-a-Chip	Werner Ogiers
12:40	13:00		Open discussion (ASIC & FPGA technology) or buffer time	
13:00	14:00		Lunch	
14:00	14:20	Atmel (F)	AT697FM (LEON2-FT) & other Standard ASICs	Bernard Bancelin
14:20	14:40	Astrium GmbH (D)	MDPA ASIC (Multi-DSP/Micro-Processor Architecture)	Tim Helfers
14:40	15:00	EADS Astrium Elancourt (F)	SCOC3 (Spacecraft Controller On Chip) development environment	Franck Koebel
15:00	15:20	Thales Aenia Space-Italia (I)	Deep Space Transponder ASIC for BepiColombo	David Joseph Fiore
15:20	15:40		coffee break	
15:40	16:00	Aeroflex Gaisler (S)	Next Generation Multipurpose Microprocessor (NGMP)	Jan Andersson
16:00	16:20	TAS Toulouse (F)	Digital Transparent Processor ASICs (CHAN, SWITCH)	Emmanuel Liegeon
16:20	16:40	Astrium Ltd.	NGP-N ASIC (Next Generation Processor)	Marc Childerhouse
16:40	17:00	Astrium GmbH (D)	FFTC ASIC (Fast Fourier Transform Coprocessor)	Timothy Pike
17:00	17:30		Open discussion (System on Chip) or buffer time	

schedule		2nd Day (March 31st, 2010)		
start time	end time	Company	Activity	Presenter
09:00	09:20	Atmel (F) / CNES (F)/ AST-UK	High pin count ASIC package	Dominique de Saint Roman
09:20	09:40	QINETIQ (UK)	Radiation Effects in Deep Submicron	Pete Truscott
09:40	10:00	STMicroelectronics (F)	Deep Sub-Micron ASIC technology & HSSL (KIPSAT)	Laurent Dugoujon
10:00	10:30	E2V(F)	Low Power Broadband 1.5 GHz ADC	Veronique Rozan
10:30	10:50		coffee break	
10:50	11:10	Kayser Threde (D)	Low Power Broadband 1.5 GHz DAC	Heinz-Volker Heyer
11:10	11:30	DUTH (GR)	Essential Telemetry Support ASIC	Themis Karafasoulis
11:30	11:50	DUTH (GR)	Bank of Radiation Hardened ADCs	Themis Karafasoulis
11:50	12:10	Omnisys(S)	HIFAS (Highly Integrated Full-custom Autocorrelation Spectrometer ASIC)	Anders Emrich
12:10	12:40		Open discussion (DSM & mixed signal) or buffer time	
13:00	14:00		Lunch	
14:00	14:20	DUTH (G)	Pressure Sensor ASIC	Themis Karafasoulis
14:20	14:40	TESAT (DE)	KNUT ASIC using DARE 180nm	Dirk Thurnes
14:40	15:00	Synopsys (P) (former Chipidea/MIPS) / Septentrio (B)	Monolithic Galileo/GPS Front-End ASIC	Mehdi Rostami
15:00	15:20		coffee break	
15:20	15:40	S3 (former ACACIA) (P)	High Speed High Resolution ADC with BISC	Bernardo Henriques
15:40	16:00	ISD (GR)	High Resolution 24bit DAC	Constantin Papadas
16:00	16:20	ISD (GR)	80S32 Functional and performance validation	Constantin Papadas
16:20	17:00		Open discussion (DSM & mixed signal) or buffer time	

schedule		3rd Day (April 1st, 2010)		
start time	end time	Company	Activity	Presenter
09:00	09:20	ESA	ESA IP Cores: present status and future	Kostas Marinis
09:20	09:40	EADS Astrium (F)	1553 Databus ASIC and IP Core	Marc Souyri
09:40	10:00	CAEN Aurelia Space (I)	CANopen IP Core	Giovanni Tuccio
10:00	10:20	Syderal (CH)	Mass Memory Controller IP Core	Pasquale Lombardi
10:20	10:40		coffee	
10:40	11:00	EADS Astrium Elancourt (F) / Magillem(F) / Aeroflex Gaisler (S)	Development of OCP sockets and IP-XACT descriptions for ESA IP cores	Aurelien Lefevre
11:00	11:30	Politecnico di Milano (I)	Development of LEON SystemC Model	Luca Fosatti
11:30	11:50	IDA Braunschweig (D)	HW/SW Co-simulation Platform	Herald Michalik
11:50	12:10	QUALTEK (B)	SpW SystemC TLM IP Core	Nkos Mouratidis
12:10	12:30	IAS (F)	SpW IP core in Atmel AT280K FPGA for BepiColombo SYMBIOSYS	Vincent Carlier
12:30	13:00		Open discussion (IP Cores) or buffer time	
13:00	13:15		CONCLUSIONS and END of MDP 2010	