



Applicability of TSC695F Application Notes or Errata Sheet to ERC32 Chipset

TSC695F Errata Sheet Applicability

Reference document: TSC695F Errata Sheet 4280D-AERO- 06/06

TSC695F Errata List	ERC32 Chipset applicability
1 - DMA Access Request During Processor LOCK Accesses	Not applicable
2 - BUSRDY* and Waitstates on Exchange Memory Area	Applicable
3 - Floating-Point Compare Single or Double Instruction followed by specific IU Instruction followed by Floating-Point Store Double Instruction	Applicable

ERC32 Chipset
TSC691 (IU)
TSC692 (FPU)
TSC693 (MEC)

Application Note

TSC695F Application Note Applicability

TSC695F Application Note	ERC32 Chipset applicability
EDAC testing Injection of correctable and uncorrectable error Reference: 4310-AERO-12/03	Applicable
Trap generation under EDAC and parity protection Reference: 4309-AERO-12/03	Applicable
Annuled Cycle Management on the TSC695 Reference: 4326-AERO-02/04	Not Applicable
Checking TSC695 code for occurrence of FCMPxx/IUop/STDF instruction sequence Reference: 7662-AERO-07/06	Applicable

4324D-AERO-07/06





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