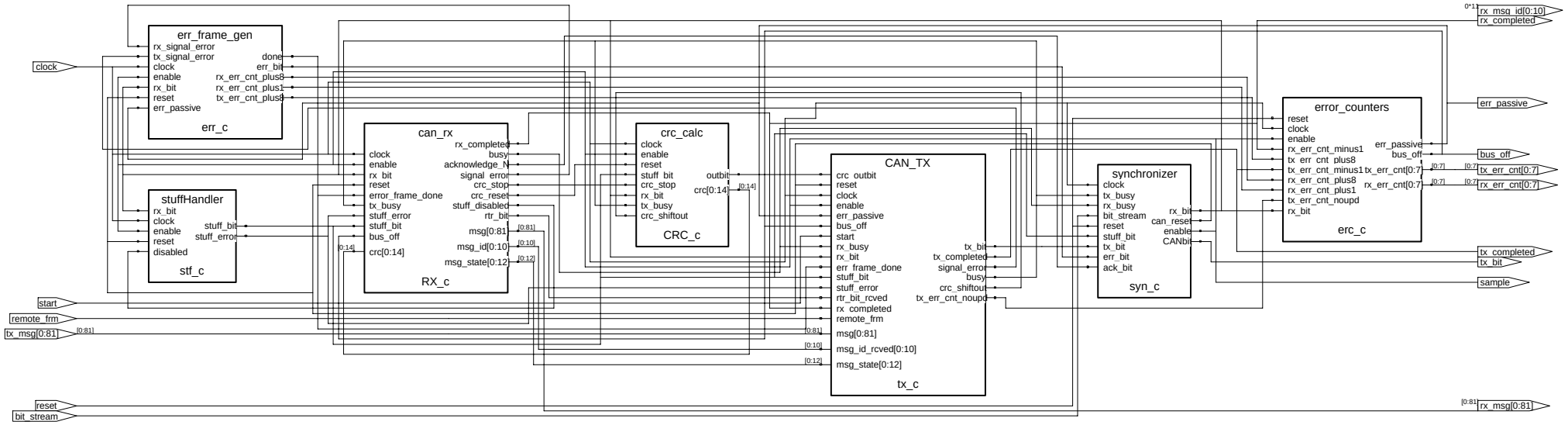
err_frame_gen
rx_signal_error
tx_signal_error
clock
enable
rx_bit
reset
err_passive
done
err_bit
rx_err_cnt_plus8
rx_err_cnt_plus1
tx_err_cnt_plus8
err_c
stuffHandler
rx_bit
clock
enable
reset
disabled
stuff_bit
stuff_error
stf_c
can_rx
clock
enable
rx_bit
reset
error_frame_done
tx_busy
stuff_error
stuff_bit
bus_off
crc[0:14]
rx_completed
busy
acknowledge_N
signal_error
crc_stop
crc_reset
stuff_disabled
rtr_bit
msg[0:81]
msg_id[0:10]
msg_state[0:12]
RX_c
crc_calc
clock
enable
reset
stuff_bit
crc_stop
rx_bit
tx_busy
crc_shiftout
outbit
crc[0:14]
CRC_c
CAN_TX
crc_outbit
reset
clock
enable
err_passive
bus_off
start
rx_busy
rx_bit
err_frame_done
stuff_bit
stuff_error
rtr_bit_rcved
rx_completed
remote_frm
msg[0:81]
msg_id_rcved[0:10]
msg_state[0:12]
tx_c
tx_bit
tx_completed
signal_error
busy
crc_shiftout
tx_err_cnt_noupd
synchronizer
clock
tx_busy
rx_busy
bit_stream
reset
stuff_bit
tx_bit
err_bit
ack_bit
rx_bit
can_reset
enable
CANbit
syn_c
error_counters
reset
clock
enable
rx_err_cnt_minus1
tx_err_cnt_plus8
tx_err_cnt_minus1
rx_err_cnt_plus8
rx_err_cnt_plus1
tx_err_cnt_noupd
rx_bit
err_passive
bus_off
tx_err_cnt[0:7]
rx_err_cnt[0:7]
erc_c
clock
start
remote_frm
tx_msg[0:81]
reset
bit_stream
rx_msg_id[0:10]
rx_completed
err_passive
bus_off
tx_err_cnt[0:7]
rx_err_cnt[0:7]
tx_completed
tx_bit
sample
rx_msg[0:81]